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### 1 [Low power optimization technique for BDD mapped circuits](#)

Per Lindgren, Mikael Kerttu, Mitch Thornton, Rolf Drechsler

 January 2001 **Proceedings of the 2001 conference on Asia South Pacific design automation**

 Full text available: pdf(184.69 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The minimization of power consumption is an important design constraint for circuits used in portable devices. The switching activity of a circuit node in a CMOS digital circuit directly contributes to overall power dissipation. By approximating the switching activity of circuit nodes as internal switching probabilities in Binary Decision Diagrams (BDDs), it is possible to estimate the dynamic power dissipation characteristic of circuits resulting from a structural mapping of a BDD. A techni ...

### 2 [Formal verification in hardware design: a survey](#)

Christoph Kern, Mark R. Greenstreet

 April 1999 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 4 Issue 2

 Full text available: pdf(411.53 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In recent years, formal methods have emerged as an alternative approach to ensuring the quality and correctness of hardware designs, overcoming some of the limitations of traditional validation techniques such as simulation and testing. There are two main aspects to the application of formal methods in a design process: the formal framework used to specify desired properties of a design and the verification techniques and tools used to reason about the relationship between a spec ...

**Keywords:** case studies, formal methods, formal verification, hardware verification, language containment, model checking, survey, theorem proving

### 3 [Combinational logic synthesis for LUT based field programmable gate arrays](#)

Jason Cong, Yuzheng Ding

 April 1996 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 1 Issue 2

 Full text available: pdf(628.91 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

The increasing popularity of the field programmable gate-array (FPGA) technology has

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